

PACT EDA Tool Patents

	Patent	Headline	Link	Notes
US	8 069 373	Method for debugging reconfigurable architectures	http://www.google.com/patents/US8069373	
US	7 996 827	Method for the translation of programs for reconfigurable architectures	http://www.google.com/patents/US7996827	
US	7 840 842	Method for debugging reconfigurable architectures	http://www.google.com/patents/US7840842	
US	7 480 825	Method for debugging reconfigurable architectures	http://www.google.com/patents/US7480825	
US	7 266 725	Method for debugging reconfigurable architectures	http://www.google.com/patents/US7266725	
US	7 210 129	Method for translating programs for reconfigurable architectures	http://www.google.com/patents/US7210129	

PACT EDA Tool Patent Applications

Application	Notes
13/177,820	
10/009,649	allowed
12/640,201	
13/279,561	
12/087,916	allowed
12/746,570	